



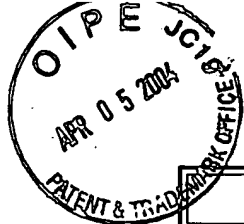
Electronic Filing System (EFS) Data
Electronic Patent Application Submission
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EFS ID: 58453
Application ID: 10671200
Title of Invention: LOW VOLTAGE CIRCUIT FOR
INTERFACING WITH HIGH
VOLTAGE ANALOG SIGNALS
First Named Inventor: CHRISTIAN Dupuy et al.
Domestic/Foreign Application: Domestic Application
Filing Date: 2003-09-25
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Statement
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Confirmation number: 6943
Attorney Docket Number: ATM-240



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Electronic Version v1.1


Stylesheet Version v1.1.0

Title of Invention	LOW VOLTAGE CIRCUIT FOR INTERFACING WITH HIGH VOLTAGE ANALOG SIGNALS																
<table border="0"><tr><td>Application Number:</td><td>10/671200</td><td></td></tr><tr><td>Date:</td><td>2003-09-25</td><td></td></tr><tr><td>First Named Applicant:</td><td colspan="2">Mr. CHRISTIAN Dupuy et al.</td></tr><tr><td>Confirmation Number:</td><td colspan="2">6943</td></tr><tr><td>Attorney Docket Number:</td><td colspan="2">ATM-240</td></tr></table>			Application Number:	10/671200		Date:	2003-09-25		First Named Applicant:	Mr. CHRISTIAN Dupuy et al.		Confirmation Number:	6943		Attorney Docket Number:	ATM-240	
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<table border="0"><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>ATM240.SupplIDS-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr></table>			Documents being submitted	Files	us-ids	ATM240.SupplIDS-usidst.xml		us-ids.dtd		us-ids.xsl							
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Electronic Version v18
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